In re Application of: Katsumi Sameshima

Serial No.: 09/451,979 Confirmation No.:9727 Filed: November 30, 1999

For: FERROELECTRIC MEMORY AND METHOD FOR

MANUFACTURING SAME Commissioner for Patents Washington, D.C. 20231

Attorney Docket No.: 362-39 RCE

I hereby certify this correspondence is being deposited with the
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Washington, D.C. 20231 on February 21, 2003

February 2	1, 2003	
Signature:	Marinent	Franci
	7,014 3	

Marguerite Franco

Sir:		ECH		
Transm	itted herewith is an Amendment in the above-identified application.	NOL:	T I	REC
	Small entity status of this application under 37 C.F.R. 1.9 and 1.27 has been established by a verified stapreviously submitted.	atement CE	<u>3</u>	MEN
	A verified statement to establish small entity status under 37 C.F.R. 1.9 and 1.27 is enclosed.	KTER	2003	<u></u>
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The fee has been calculated as shown below:

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FIRST PRESENTATION OF MULTIPLE DEPENDENT CLAIMS

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	Please charge my Deposit Account No in the amount of S A duplicate copy of this sheet is attached.
	A check in the amount of S is attached.
Þ	The Commissioner is hereby authorized to charge any fees or additional fees associated with this communication or credit any overpayment to Deposit Account No. 502335. A duplicate copy of this sheet is attached.
	Any filing fees under 37 C.F.R. 1.16 for the presentation of extra claims.
	Any patent application processing fees under 37 C.F.R. 1.17.
	Respectfully submitted.

Gerald T. Bodner Registration No. 30,449

Bodner & O'Rourke, LLP 425 Broadhollow Road Suite 108 Melville, New York 11747

ITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Katsumi Sameshima

Examiner:

Wai-Sing Louie

Serial No.:

Group Art Unit:

Filed: November 30, 1999

Docket:

362-39 RCE

For:

FERROELECTRIC MEMORY AND METHOD FOR MANUFACTURING SAME Confirmation No.: 9727

Commissioner for Patents

Washington, D.C. 20231

Dated: February 21, 2003

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February 21, 2003

Signature: Margnerte Marguerite Franco

REPLY TO FINAL OFFICE ACTION

Sir:

In response to the Office Action mailed October 21, 2002, please amend the aboveidentified application as follows:

IN THE CLAIMS:

Please amend Claims 2 and 3 by rewriting the same as follows:

2. (Thrice Amended) A ferroelectric memory, comprising:

an insulation film having a hollow at a top surface; and

FECHNÜLÜGY CEMTER 2800

a laminated body obtained by laminating a plurality of layers on said top surface and etching a region of said plurality of layers corresponding to a region other than said hollow, wherein said laminated body includes a lower electrode layer, a ferroelectric layer formed on said lower electrode layer and an upper electrode layer formed on said ferroelectric layer; and the memory further comprising another film embedded from said top surface of said

nonlow and separating between said insulation him and said lower electrode rayer in said